

[Name of the Document] Abstract

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[Object] The object of the present invention is to provide a circuit protector capable of suppressing the variation of fusing characteristics.

[Means to Solve the Problems] Conductive layer 12 is disposed on columnar substrate 11, and groove 13 is formed in the conductive layer 12, thereby providing narrow portion 13a, and the pore area per unit area of a surface of the substrate 11 or a portion near the surface of the substrate 11 ranges from 1% to 30%.

[Selected drawing] Fig. 1